



ALTERNATIVE TO PTO/SB/08A/B (09/06)

Substitute for form 1449/PTO			Complete if Known		
			Application Number	10/713,753	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)			Filing Date	November 13, 2003	
			First Named Inventor	Bruce W. MCGAUGHY	
			Art Unit	2123	
			Examiner Name	A. Pierre-Louise	
Sheet	1	of	1	Attorney Docket Number	188122002000

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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		Country Code ² -Number ³ -Kind Code ⁴ (if known)	MM-DD-YYYY			

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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
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	14.	Jones et al. (February 1994). "A Cache-Based Method for Acceleration Switch-Level Simulation," <i>IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems</i> 13(2).	
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	16.	Wong, Y. (1985). "Hierarchical Circuit Verification," <i>IEEE 22nd Design Automation Conference</i> .	

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Examiner Signature		Date Considered	5/11/07
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